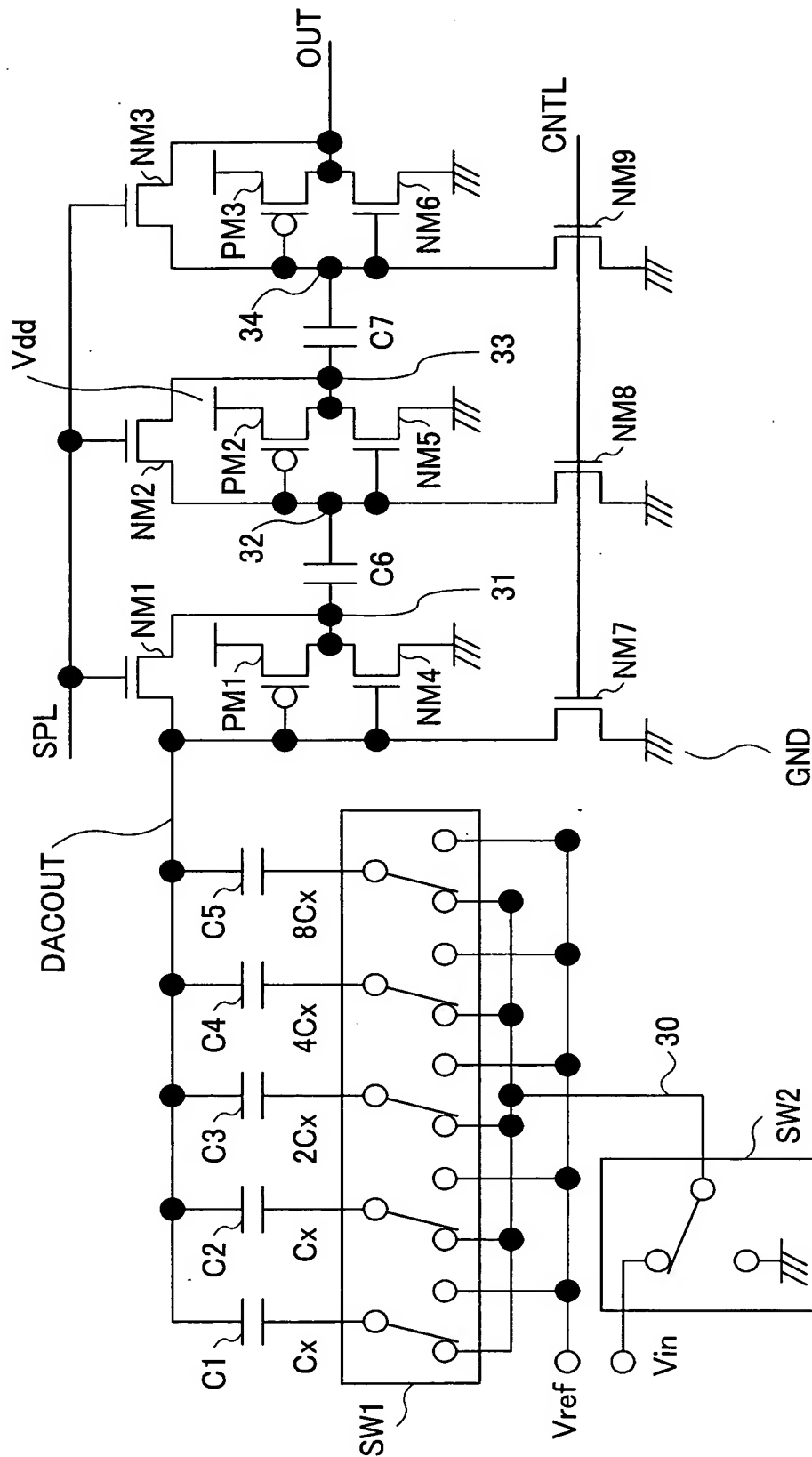


FIG.1



Title: AD CONVERTER WITH REDUCED CURRENT CONSUMPTION

Inventor's Name: TACHIBANA et al

Application No.: New Application

Docket No.: 100353-00183

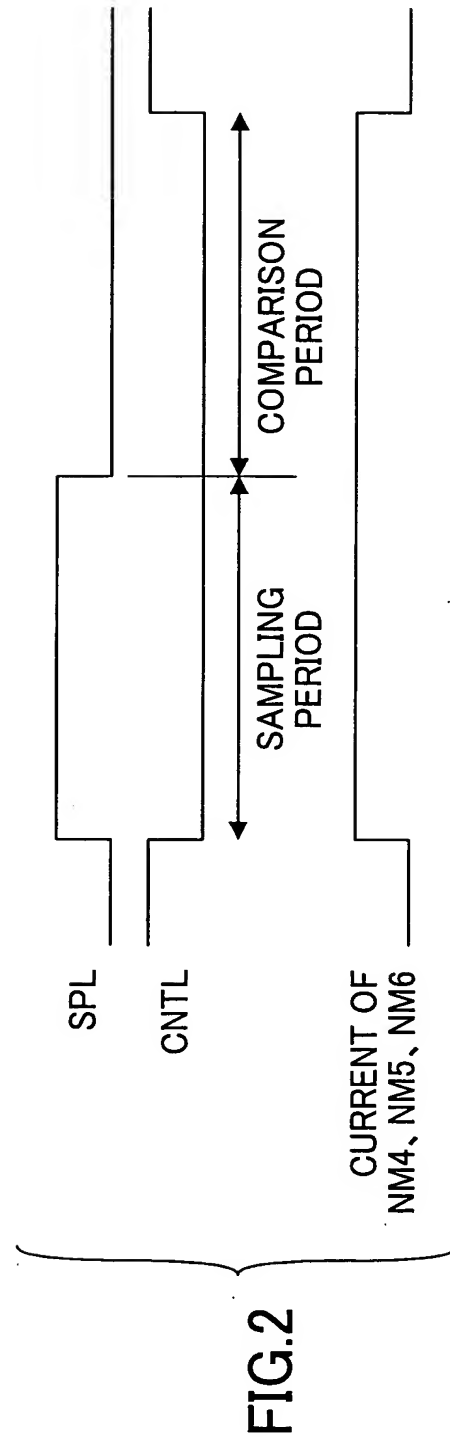
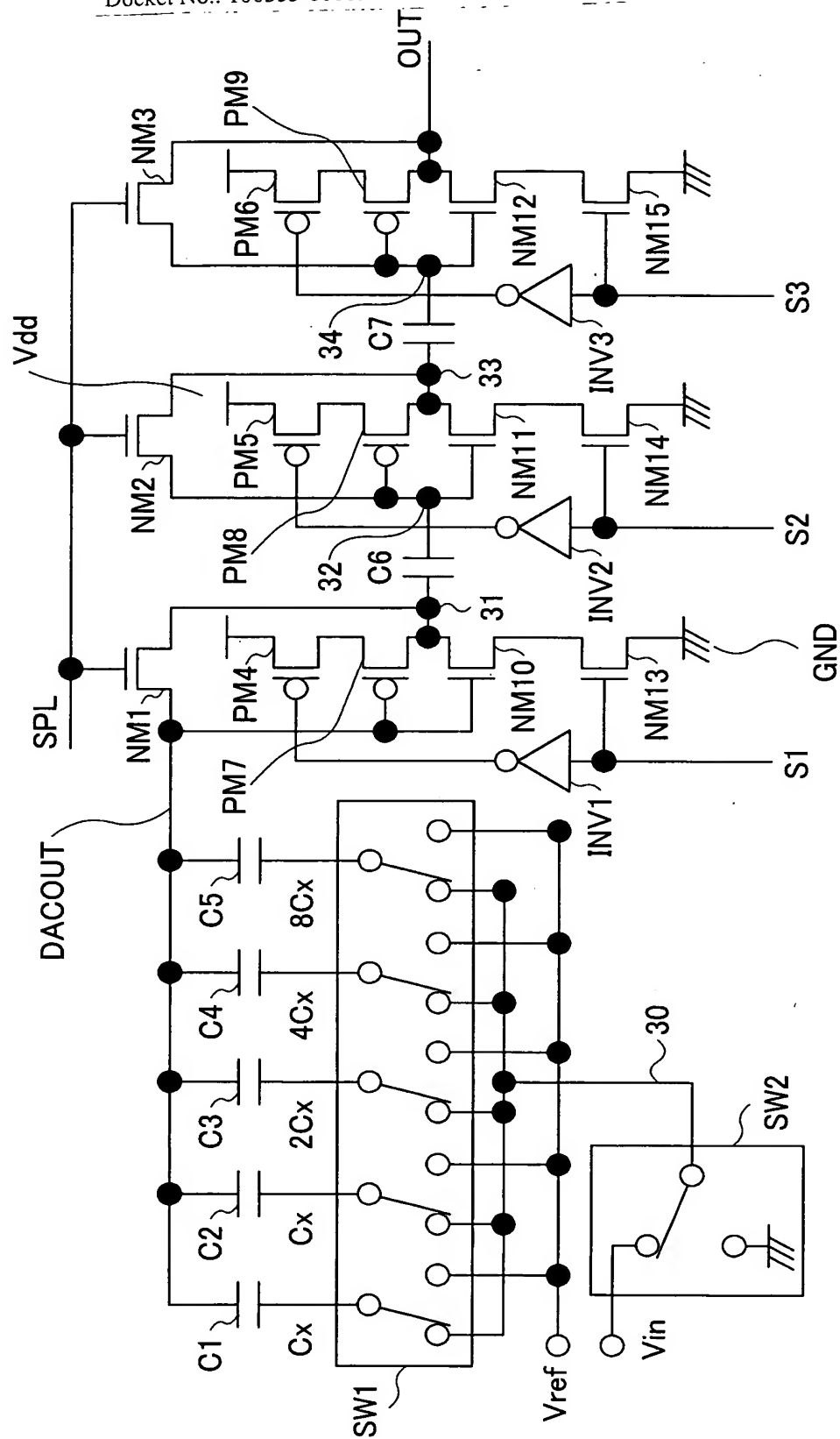


FIG.3



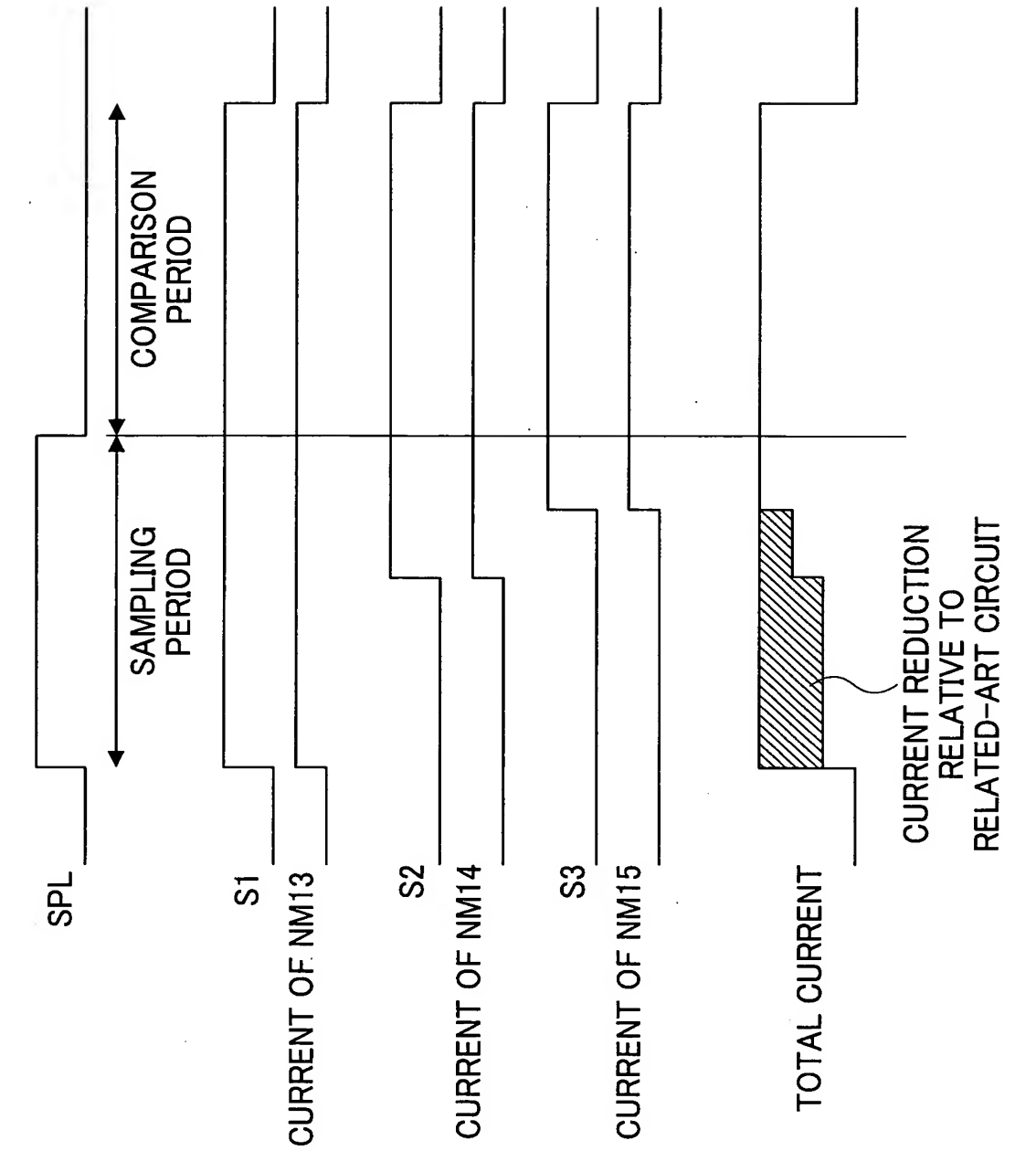
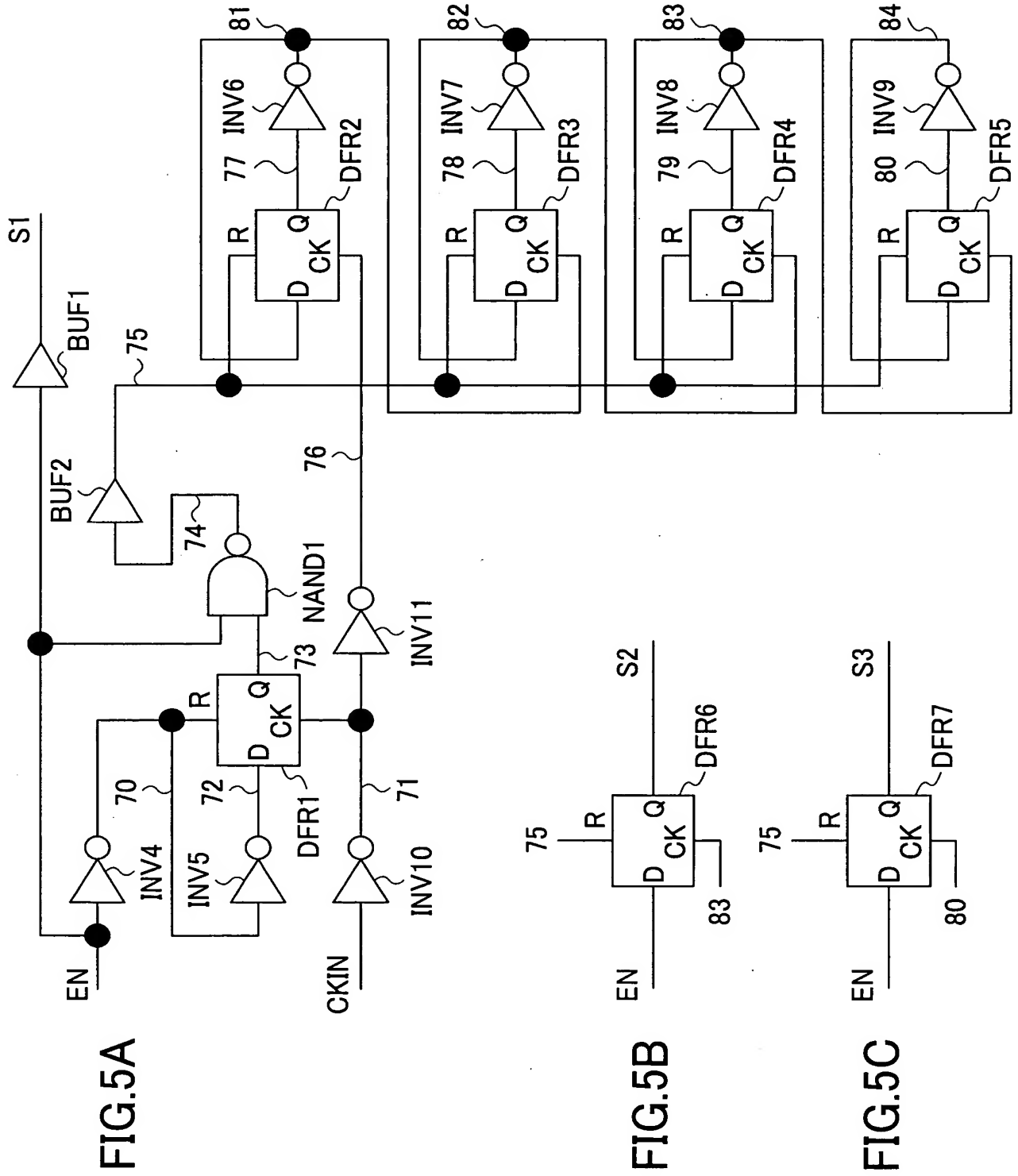


FIG.4



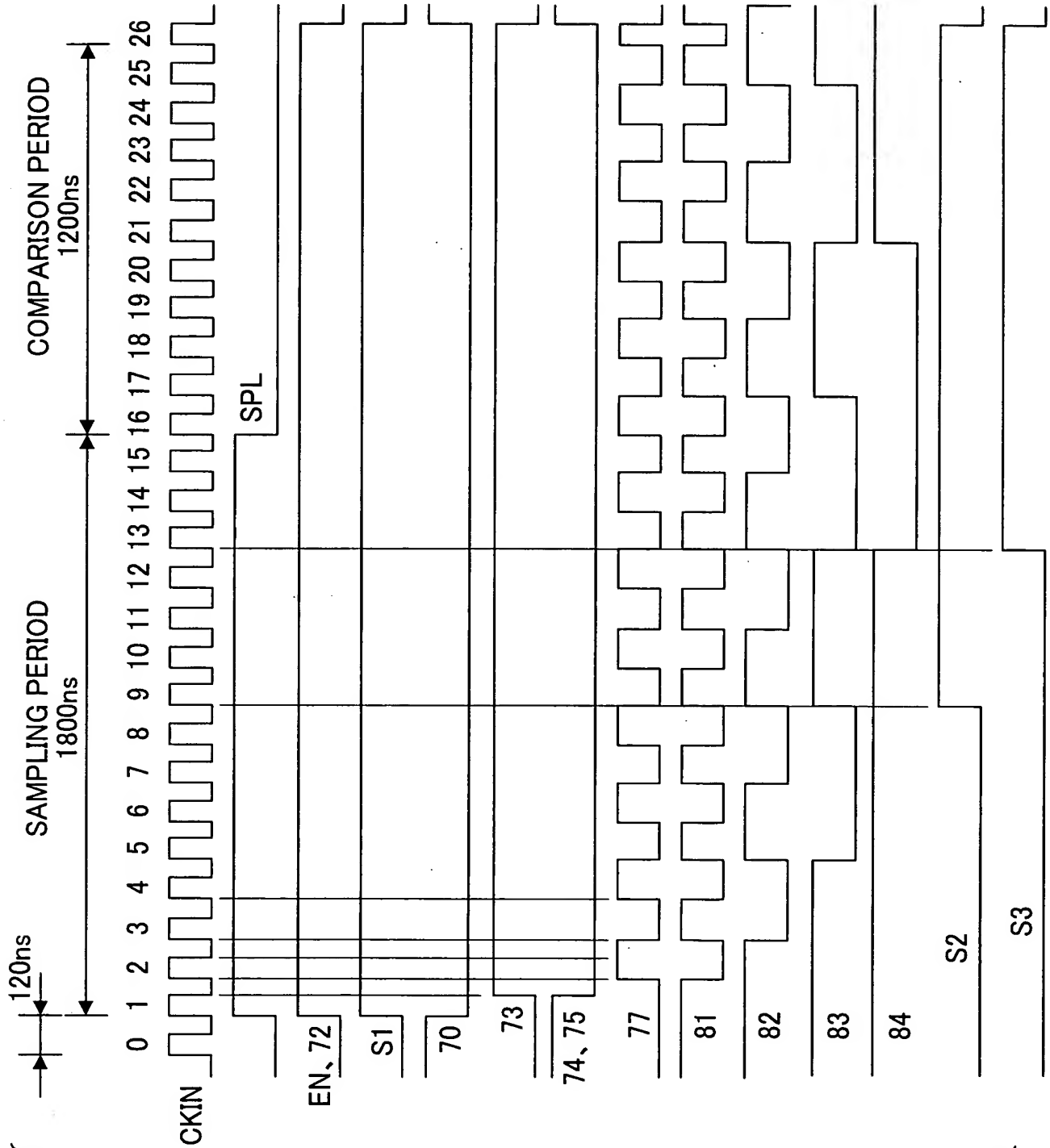


FIG.6

[illegible]

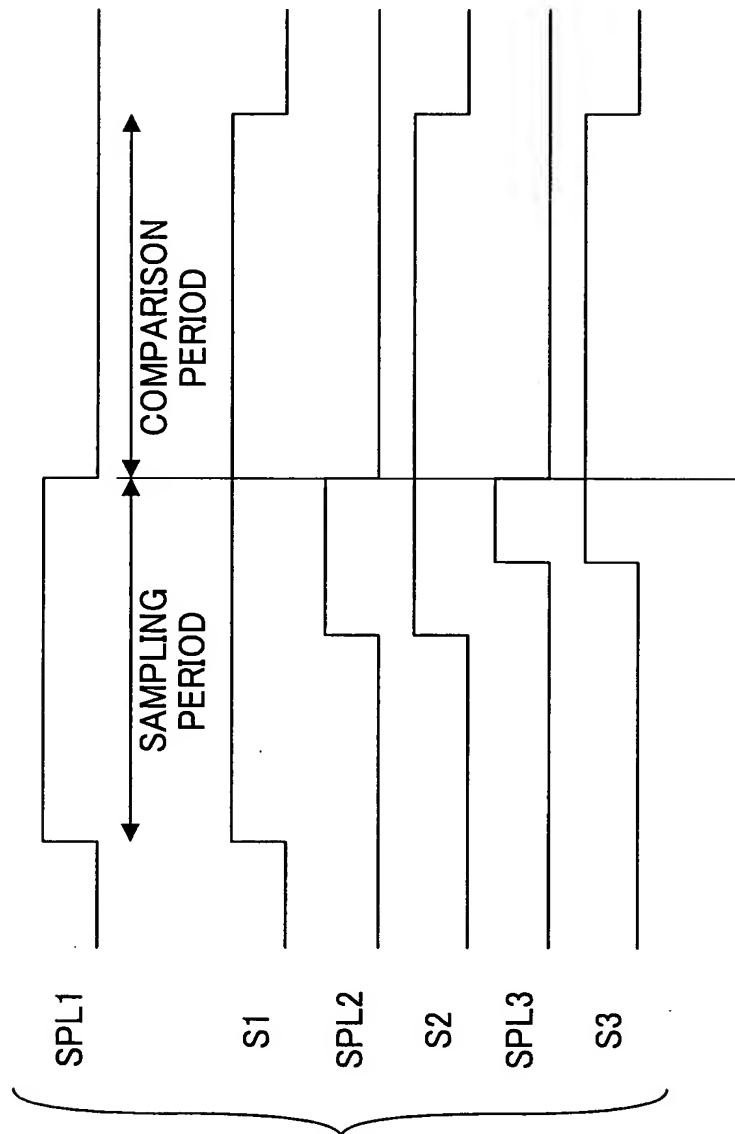
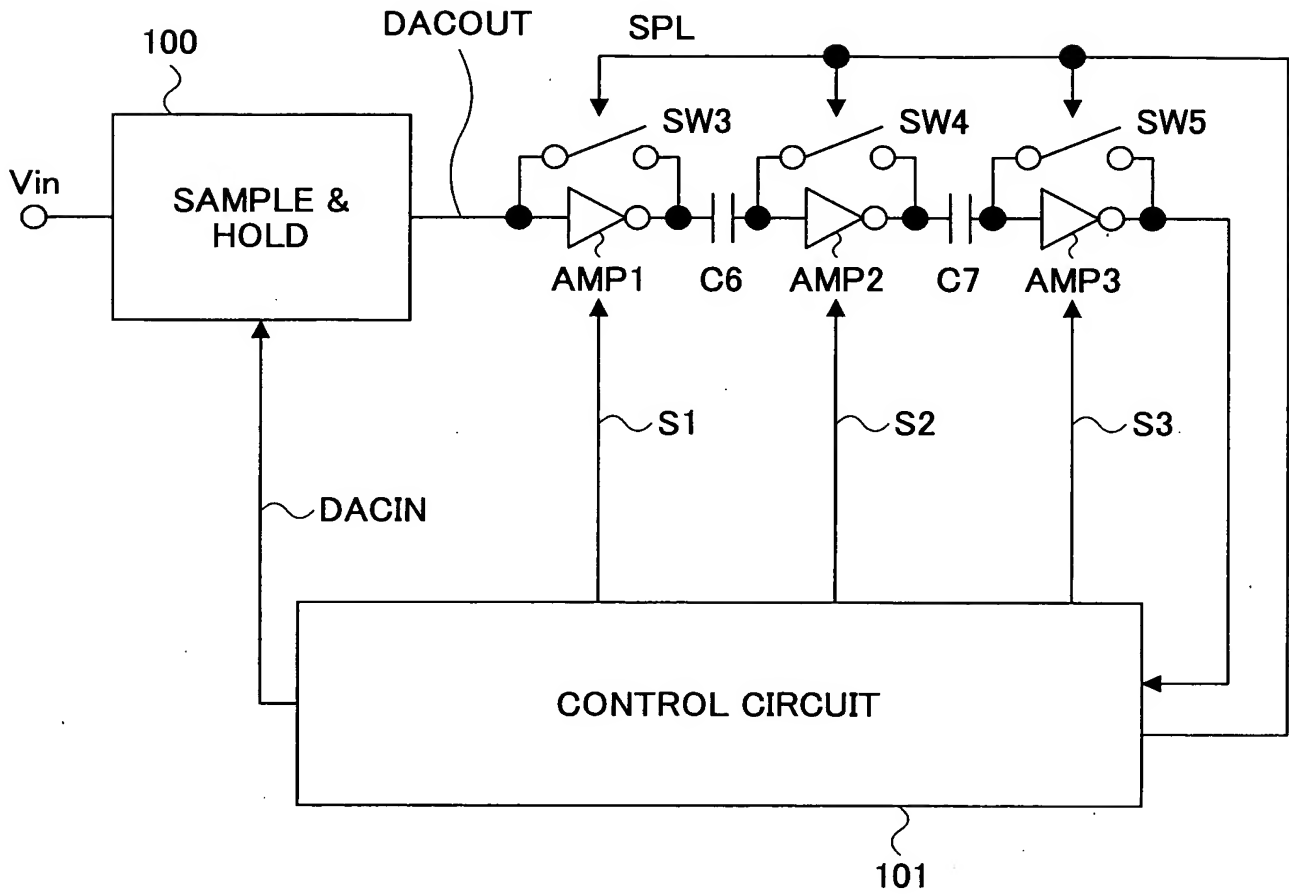


FIG.8

FIG.9



Docket No.: 100353-00183

100A

V_{in}

SAMPLE & HOLD

DACOUT

SW3

C6

SW4

C7

SW5

SW6

AMP4

AMP5

SW7

C8

C9

AMP6

SW8

DACOUT'

DACIN

S1

S2

S3

CONTROL CIRCUIT

101A

SPL